

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application for:

G.R. Mohan Rao

Application No.: 10/665,906

Patent No.: 7,707,330

For: MEMORIES FOR ELECTRONIC  
SYSTEMS

Examiner: Gary J. Portka

Art Unit: 2187

Filing Date: September 18, 2003

Confirmation No.: 7033

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Mail Stop Certificate of Corrections Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 CFR 1.322**

Dear Commissioner:

Upon review of the above-referenced Letters Patent, an error was noted that was the mistake of the U.S. Patent and Trademark Office.

Please correct the following:

at col. 13, line 23, "circuitry is farther configured" should read --circuitry is further configured--.

The correct language can be found in claim 16 on page 5 of the amendment filed in the present application on October 20, 2009.

Since this error is that of the U.S. Patent Office, it is believed that no fee is required for this correction. It is respectfully requested that a Certificate of Correction be issued.

The Commissioner is hereby authorized to charge shortages or credit overpayments to Deposit Account No. 500393.

Respectfully submitted,  
SCHWABE, WILLIAMSON & WYATT, P.C.

Date: June 28, 2010 /Richard B. Leggett/  
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